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PATENT
Case No. AUS920010735US1
(9000/70)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

JOHNNY R. BREZINA, BRIAN M. KERRIGAN,
GERALD D. MALAGRINO, JR., JAMES R. MOON

Serial No.

Filed:

Title: PACKAGING ARCHITECTURE FOR A
MULTIPLE ARRAY TRANSCEIVER USING
A FLEXIBLE CABLE AND STIFFENER FOR
CUSTOMER ATTACHMENT

Examiner:

Group Art Unit:

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

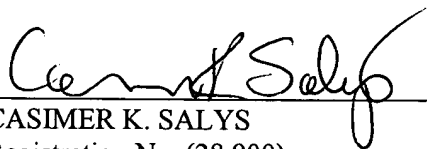
Pursuant to 37 C.F.R. §§ 1.56 and 1.97-1.98, Applicants would like to call the Examiner's attention to the references listed on the enclosed form PTO-1449. Copies of these references are also enclosed.

Applicants believe that none of the listed references, alone or in combination disclose or suggest Applicant's claimed invention.

Dated: 11/2/01

Respectfully submitted,

IBM CORPORATION
Intellectual Property Law Department
11400 Burnet Road - 4054
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FORM PTO-1449	SERIAL NO.	CASE NO. AUS920010735US1 (9000/70)
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	FILING DATE	GROUP ART UNIT
(use several sheets if necessary)	APPLICANT(S): JOHNNY R. BREZINA BRIAN M. KERRIGAN GERALD D. MALAGRINO, JR. JAMES R. MOON	

11046 U.S. PTO
 10/006838
 12/05/01

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	FILING DATE	NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	
	A1	U.S. Patent Application entitled "PACKAGING ARCHITECTURE FOR A MULTIPLE ARRAY TRANSCEIVER USING A CONTINUOUS FLEXIBLE CIRCUIT", (Inventors Johnny R. Brezina, et al.)
	A2	U.S. Patent Application entitled "FLEXIBLE CABLE STIFFENER FOR AN OPTICAL TRANSCEIVER", (Inventors Johnny R. Brezina, et al.)
	A3	U.S. Patent Application entitled "ENHANCED FOLDED FLEXIBLE CABLE PACKAGING FOR USE IN OPTICAL TRANSCEIVERS", (Inventors Johnny R. Brezina, et al.)
	A4	U.S. Patent Application entitled "PACKAGING ARCHITECTURE FOR A MULTIPLE ARRAY TRANSCEIVER USING A FLEXIBLE CABLE", (Inventors Johnny R. Brezina, et al.)
	A5	U.S. Patent Application entitled "PACKAGING ARCHITECTURE FOR A MULTIPLE ARRAY TRANSCEIVER USING A WINGED FLEXIBLE CABLE FOR OPTIMAL WIRING", (Inventors Johnny R. Brezina, et al.)
	A6	U.S. Patent Application entitled "HORIZONTAL CARRIER ASSEMBLY FOR MULTIPLE ARRAY OPTOELECTRONIC DEVICES", (Inventors Johnny R. Brezina, et al.)

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.